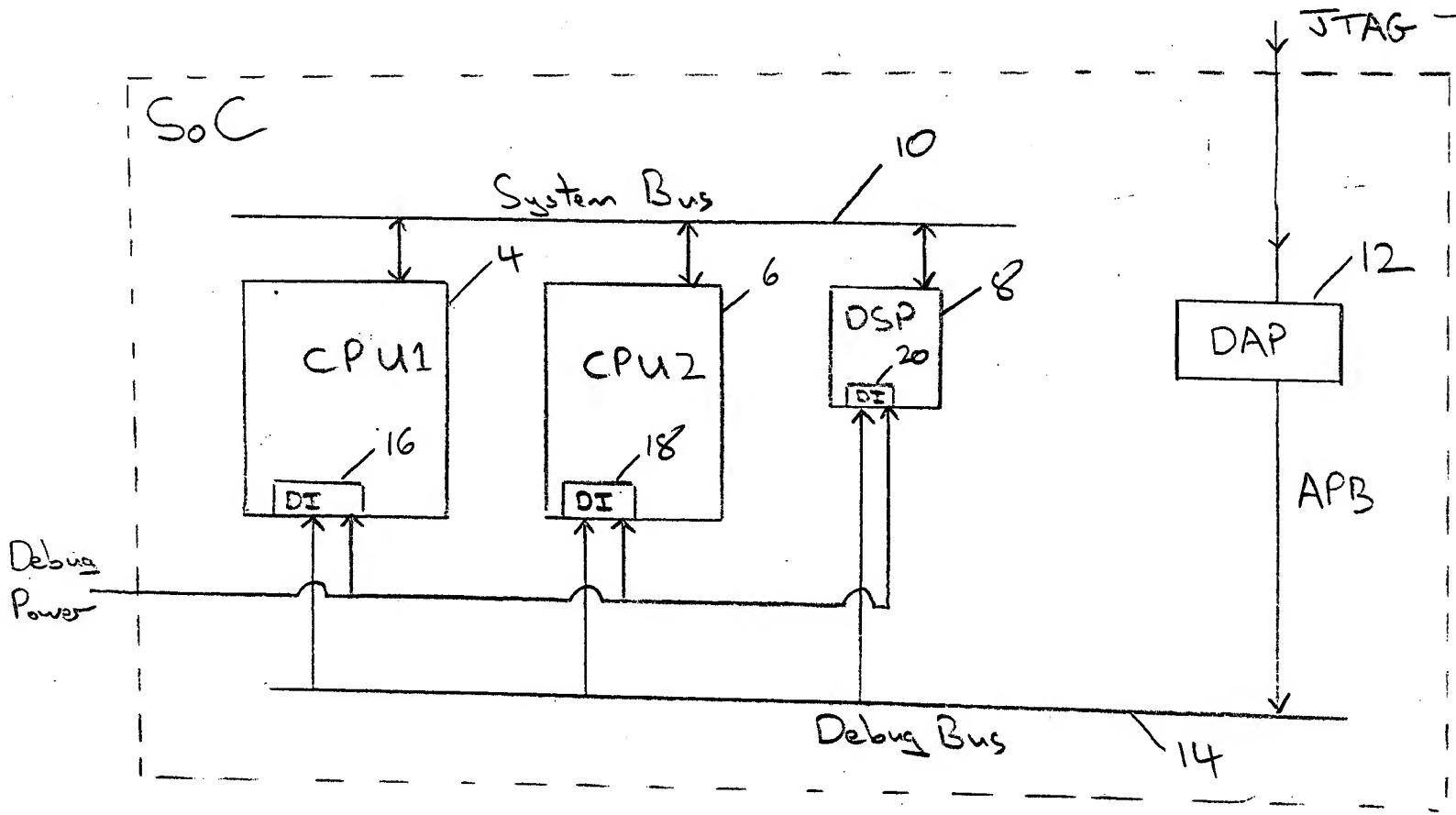

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Fig. 1

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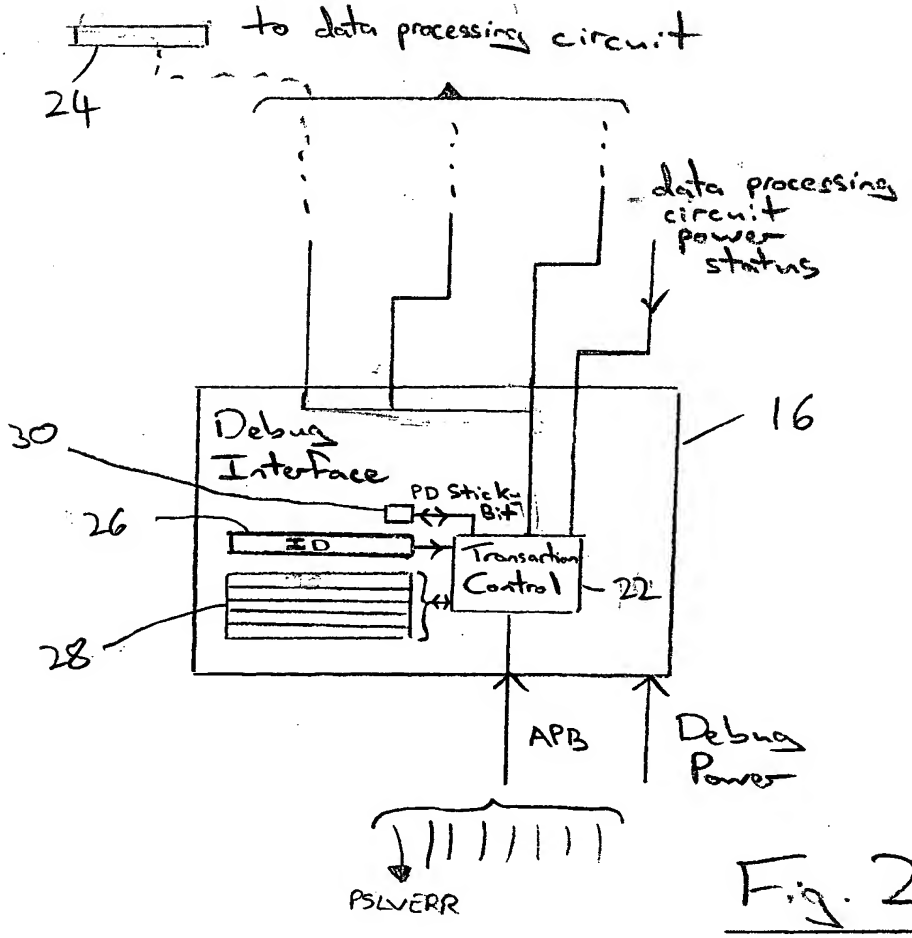

Fig. 2

Power Down Sticky Bit Setting

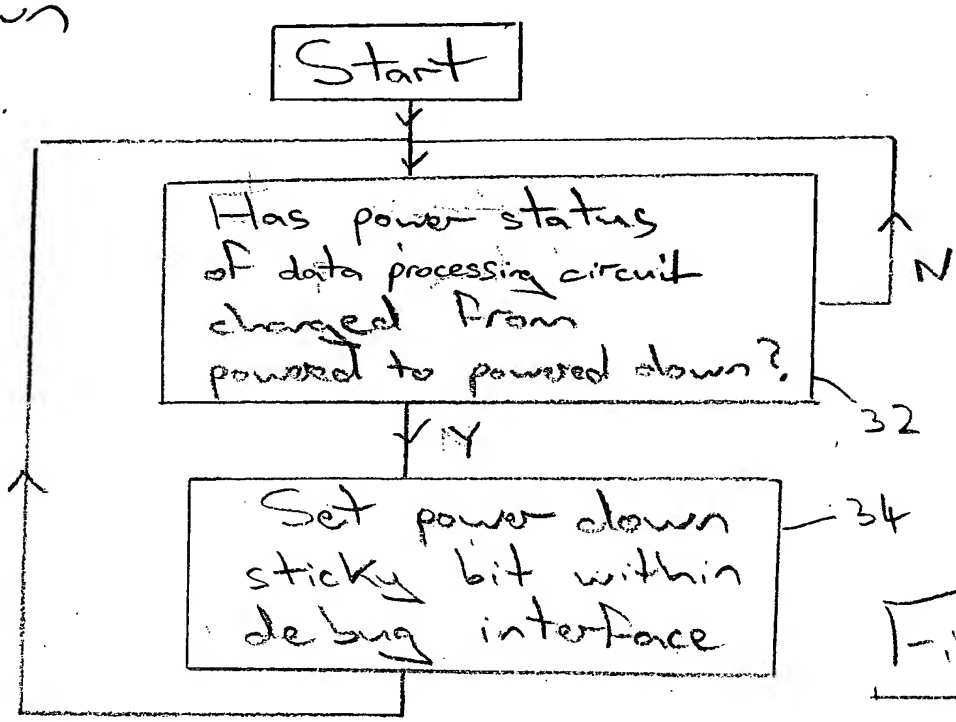

Fig. 3

Debug Bus Error Generation

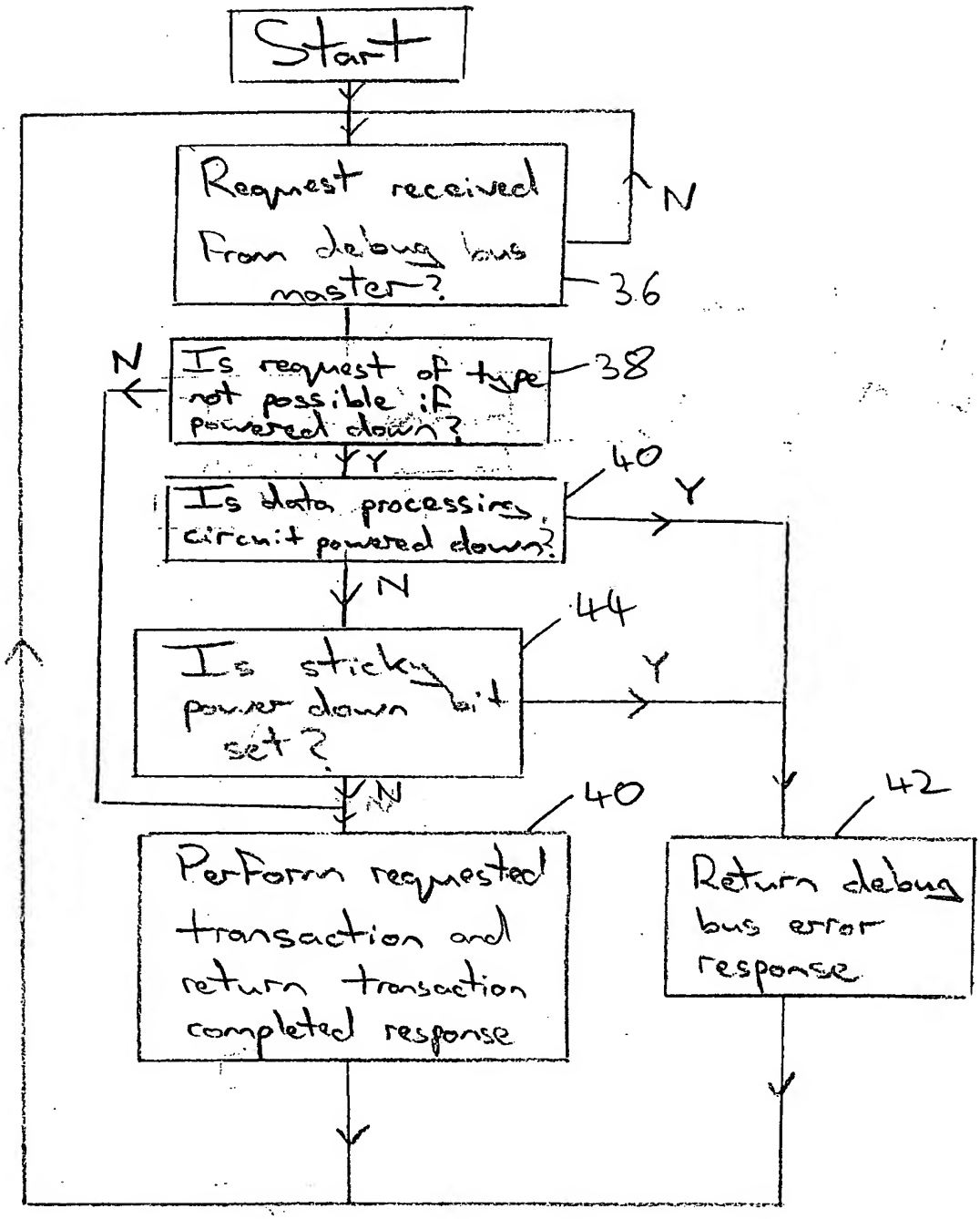

Fig. 4